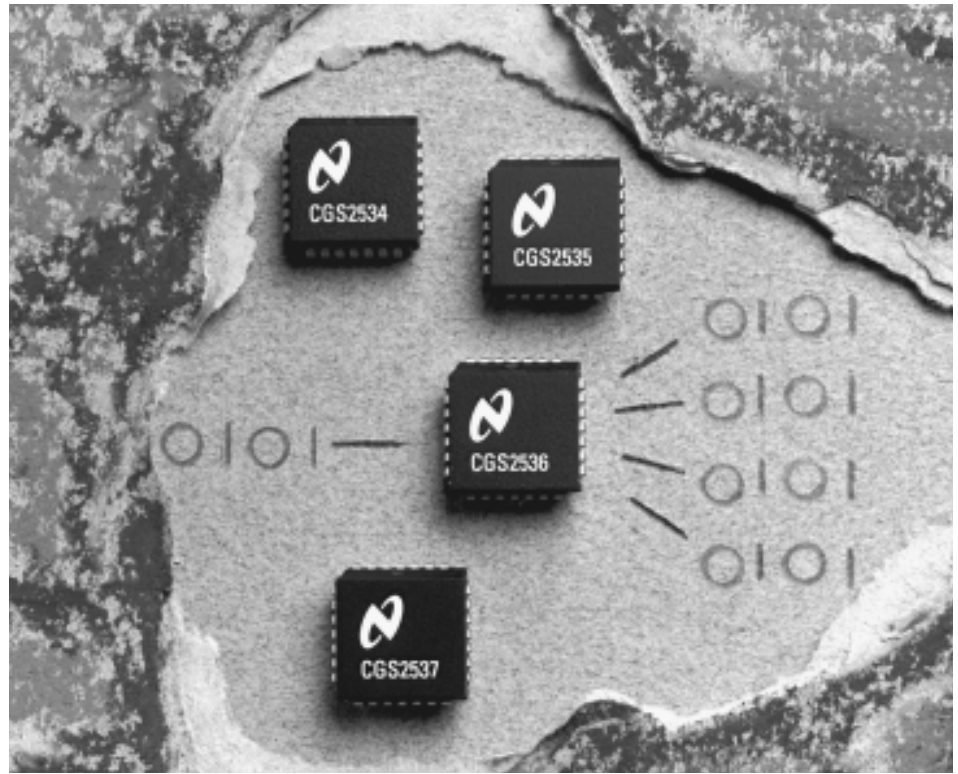


CGS253x Quad 1 to 4 Clock Drivers

- Pin-to-Pin Skew of Less Than 350 ps
- Part-to-Part Skew of Less Than 650 ps
- Output Series Resistor Integrated Into CGS2537
- Supports TTL and CMOS Output Clock Rates to 125 MHz
- Accepts TTL and CMOS Inputs
- Maximum Propagation Delays of 3.5 to 4.5 ns
- Greater Than 125 MHz Maximum Frequency
- 5V/3.3V Options
- 1X and 0.5X Output Capability

National Semiconductor's Clock Generation and Support (CGS*) family offers you a complete portfolio of clock generation and distribution timing solutions. The newest family of high performance clock drivers offers you the highest performance and flexibility available in a cost-effective device – High speed as measured by maximum frequency, accuracy as measured by skew, and flexibility as measured by supply voltages and output configurations. National's clock drivers save time and effort by offering synchronous signals that reduce timing errors and system failures. These clock drivers also integrate output series resistors that reduce undershoot and simplify line termination design. They further aid designers by offering high fanout that reduces component count and saves board real estate. In addition, for today's mixed voltage systems, the inputs are 5V tolerant when using a 3.3V supply. National's CGS253x family of clock drivers (CGS2534V/CGS2535V/CGS2536V/CGS2537V) is optimized to drive large loads with sub-3.5ns propagation delays, 1.5ns edge rates, and 36mA drive.

IBIS models for the CGS253x, along with all of National's clock generators and drivers, can be downloaded (via FTP) from the Internet at vhdl.org, where all of the semiconductor industry's IBIS models are



located. In addition, one can access the models at URL: <http://www/vhdl.org/pub/ibis> (and click on 'models' and 'national') or using a modem to dial up (415) 335-0110. IBIS (I/O Buffer Information Specification) is a fast and accurate behavioral method of modeling I/O buffers based on V/I curve data derived from measurements or full circuit simulation. IBIS uses a standardized software parsable format to create the behavioral information needed to model analog characteristics of ICs and can be used by almost any analog simulator/EDA tool in the industry. IBIS is developed by the IBIS Open Forum and most forum activities are handled through email discussions using the reflector ibis@vhdl.org.

PROCESSORS SUPPORTED:
80186, 80C186, 80C186XL/EA/EB/EC, 80L186EA/EB/EC, 80188, 80C188, 80C188XL/EA/EB/EC, 80L188EA/EB/EC, Intel386™ CX/EX/SX/SXSA/DX, and Intel486™ SX, IntelDX2™, IntelDX4™ Processors

AVAILABILITY:
Now

CONTACT:
National Semiconductor
Phone: (800) 272-9959
For international contacts see Appendix B.